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BIB DATA SHEET

CONFIRMATION NO. 6384

SERIAL NUMBER	FILING or 371(c) DATE	CLASS	GROUP ART UNIT	ATTORNEY DOCKET NO.
10/073,948	02/14/2002 RULE	712	2183	0120-024

APPLICANTS
 John Rhoades, Bristol, UNITED KINGDOM;
 Ken Cameron, Bristol, UNITED KINGDOM;
 Paul Winsor, Bristol, UNITED KINGDOM;
 Ray McConnell, Bristol, UNITED KINGDOM;
 Gordon Faulds, Gloucestershire, UNITED KINGDOM;
 Simon McIntosh-Smith, South Gloucestershire, UNITED KINGDOM;
 Anthony Spencer, Gloucestershire, UNITED KINGDOM;
 Jeff Bond, Barry, UNITED KINGDOM;
 Matthias Dejaegher, Bradley Stoke, UNITED KINGDOM;
 Danny Halamish, Bristol, UNITED KINGDOM;
 Gajinder Panesar, Bristol, UNITED KINGDOM;

**** CONTINUING DATA *******

**** FOREIGN APPLICATIONS *******
 UNITED KINGDOM 0103678.9 02/14/2001
 UNITED KINGDOM 0103687.0 02/14/2001
 UNITED KINGDOM 0121790.0 09/10/2001

**** IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** ** SMALL ENTITY ****
 04/03/2002

Foreign Priority claimed 35 USC 119(a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No (KEITH E VICARY) Examiner's Signature	<input type="checkbox"/> Met after Allowance 10/07	STATE OR COUNTRY UNITED KINGDOM	SHEETS DRAWINGS 9	TOTAL CLAIMS 33	INDEPENDENT CLAIMS 1
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ADDRESS
 POTOMAC PATENT GROUP PLLC
 P. O. BOX 270
 FREDERICKSBURG, VA 22404
 UNITED STATES

KV
 7/11/2010

TITLE
 Data processing architectures for packet handling wherein batches of data packets of unpredictable size are distributed across processing elements arranged in a SIMD array operable to process different respective packet protocols at once while executing a single common instruction stream

FILING FEE RECEIVED 987	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees (Filing)
		<input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
		<input type="checkbox"/> 1.18 Fees (Issue)
		<input type="checkbox"/> Other _____